



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/765,881	01/16/2001	Vernon E. Mc George JR.	10992444-1	9291

7590 07/08/2005

HEWLETT-PACKARD COMPANY
Intellectual Property Administration
P.O. Box 272400
Fort Collins, CO 80527-2400

EXAMINER

CHRISTMAN, KATHLEEN M

ART UNIT	PAPER NUMBER
----------	--------------

3713

DATE MAILED: 07/08/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/765,881

Applicant(s)

MC GEORGE ET AL.

Examiner

Kathleen M. Christman

Art Unit

3713

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 04/27/05.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-19 is/are pending in the application.
- 4a) Of the above claim(s) 7-17 is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-6, 18 and 19 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

Art Unit: 3713

DETAILED ACTION

In response to the amendment filed 04/27/2005; claims 1-19 are pending; claims 7-17 remain withdrawn from consideration.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

The factual inquiries set forth in *Graham v. John Deere Co.*, 383 U.S. 1, 148 USPQ 459 (1966), that are applied for establishing a background for determining obviousness under 35 U.S.C. 103(a) are summarized as follows:

1. Determining the scope and contents of the prior art.
2. Ascertaining the differences between the prior art and the claims at issue.
3. Resolving the level of ordinary skill in the pertinent art.
4. Considering objective evidence present in the application indicating obviousness or nonobviousness.

1. Claims 1-6, 18 and 19 are rejected under 35 U.S.C. 103(a) as being unpatentable over Peters et al (US 5842195). Peters et al teaches a computer based method, system and computer readable medium including: defining a question for presentation in a survey (the question text, col. 14: 26-31); defining a plurality of potential answers, each one of said plurality of potential answers corresponding to said question (the option text, col. 14: 57-64); assigning a question code to said question (the database field name, col. 14: 65+); assigning a plurality of choice codes, one of each said choice codes assigned uniquely to each one of said plurality of potential answers (the "on selection database field value", col. 15: 53-59); creating question choice records for associating question code with each choice code (the database itself); presenting said question to a survey participant, and presenting said set of potential answers to said survey participant in accordance with said question choice records (the actual

Art Unit: 3713

administration of the survey, col. 32: 24-50), as in **claim 1**, and substantially similar limitations in **claims 18 and 19**. Creating an answer record to associate said choice code of a selected answer with said question code (**claim 2**), is taught in Figure 14. Assigning a choice type to said choice code to define the format of the answer solicited by said question (**claim 3**) is shown in at least col. 12: 55 - col. 13: 17. The answer format may be multiple-choice format, col. 12: 57-59, and further allows for entry of both numeric and text data, col. 13: 1-3. The author is allowed to specify a follow-up question for a predetermined said potential answer (**claim 5**), said follow-up questions being presented to the survey participant (**claim 6**), as is shown in at least col. 13: 44-46 and col. 15: 23-36, as a "Go To" or "Branch-To" question.

Peters et al does not specifically teach that use of an application specific integrated circuit (**claims 1-6, 18 and 19**); storing the information in "flash" memory (**claims 1-6**); using logic gates with logic functions (**claims 1-6**); and a logic circuit with discrete logic gates (**claim 18** and the representative structure associated with the "discrete logic means" in **claim 19**). The examiner notes, each of these features was added with the amendment dated 04/27/2005.

On page 12, starting at line 21 of the specification as originally filed applicant admits that a discrete logic circuit having logic gates for implementing logic functions upon data signals, an application specific integrated circuit having appropriate combinational logic gates, a programmable gate array, and field programmable gate arrays are all old and well-known in the art. These components are also art-accepted equivalents for the implementation of any software methodology. Further the examiner takes OFFICIAL NOTICE that flash memories are old and well-known in the art of computer architecture and is an art accepted equivalent to the memory which is inherently present in the database structure of the Peters et al system. It would have obvious to one of ordinary skill in the art to implement the method and system using application specific integrated circuits which make use of logic gates, flash memory and/or discrete logic gates so as to produce a dedicated system focused entirely on solving a single problem (collecting survey data).

Art Unit: 3713

Response to Arguments

2. Applicant's arguments with respect to claims 1-6, 18 and 19 have been considered but are moot in view of the new ground(s) of rejection.

Conclusion

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kathleen M. Christman whose telephone number is (571) 272-4435. The examiner can normally be reached on M-F 8:00-4:00.

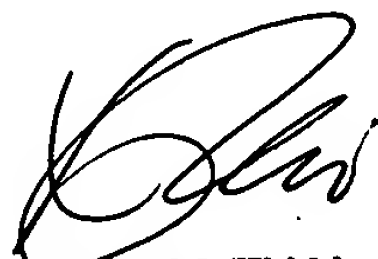
If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Xuan Thai can be reached on (571) 272-7147. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Art Unit: 3713

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Kathleen M. Christman
June 28, 2005



XUAN M. THAI
SUPERVISORY PATENT EXAMINER

TC3700